

In the Drawings:

The attached drawing sheets include changes to Figs. 9, 11-17, 23, and 25-28.

The Annotated sheets showing the changes are attached. The Replacement sheets replace the originally filed sheets.

REMARKS

The drawings have been amended as required. Additional amendments correct typographical errors. Withdrawal of the objection to the drawings is requested.

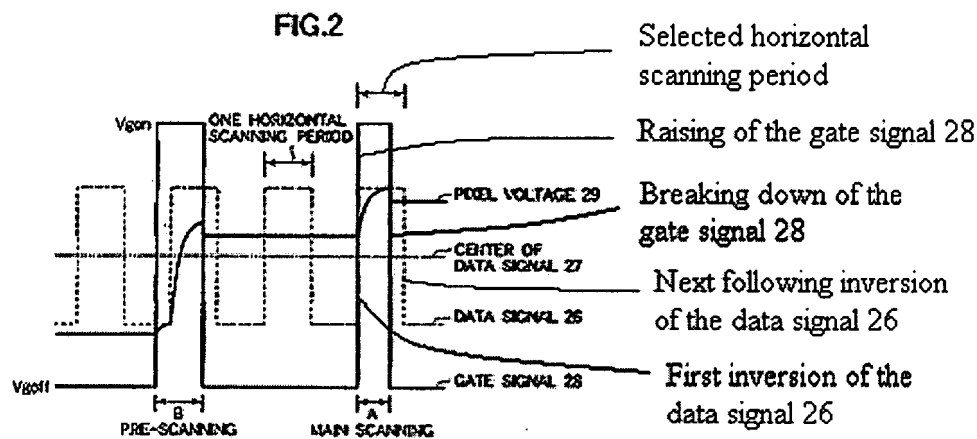
Claims 1-2 and 5-12 stand rejected under §102 on the basis of Applicant's Admitted Prior Art (AAPA). Applicant traverses this rejection because Claim 1, as amended, first defines, *inter alia*, that for a selected horizontal scanning period, the gate signal is raised in the main scanning at a timing on or after a first inversion of the data signal and that the gate signal is broken down in the main scanning at a timing prior to the next following inversion of the data signal.

Applicant's Prior Art Figure 26 illustrates one frame period. One frame period includes a plurality of horizontal scanning periods. In other words, one frame period is different from one horizontal scanning period. Fig. 26 illustrates that a polarity of a data signal is inverted for every frame period. Therefore, Fig. 26 does not illustrate that a data signal is used whose polarity is inverted at the beginning and end of each horizontal scanning period, as defined in independent Claim 1.

Furthermore, Fig. 26 does not illustrate one horizontal scanning period, and thus does not show a relation between a selected horizontal scanning period and the timings of raising and breaking down of a gate signal. Therefore, Fig. 26 does not illustrate that the gate signal is raised in the main scanning at a timing on or after a first insertion of the data

signal occurring during the selected horizontal scanning period, and the gate signal is broken down in the main scanning at a timing prior to the next following inversion of the data signal occurring during the selected horizontal scanning period, as defined in independent Claim 1.

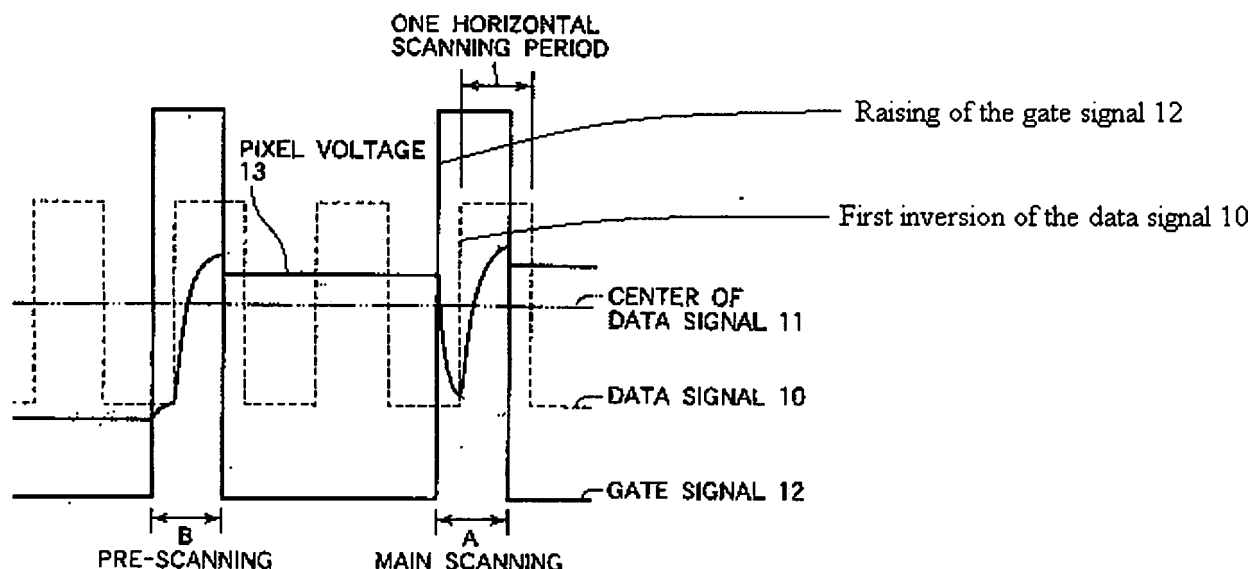
Fig. 2 (shown below with notations) illustrates another feature of amended Claim 1, where for the selected horizontal scanning period, the gate signal 28 is raised in the main scanning at a timing on or after a first inversion of the data signal 26, and the gate signal 28 is broken down in the main scanning prior to the next following inversion of the data signal 26.



This differs from the admitted prior art of Fig. 27 (shown below with notations) and Fig. 28 (not shown), where the gate signal 12 is raised in the main scanning prior to the first inversion of the data signal 10.

PRIOR ART

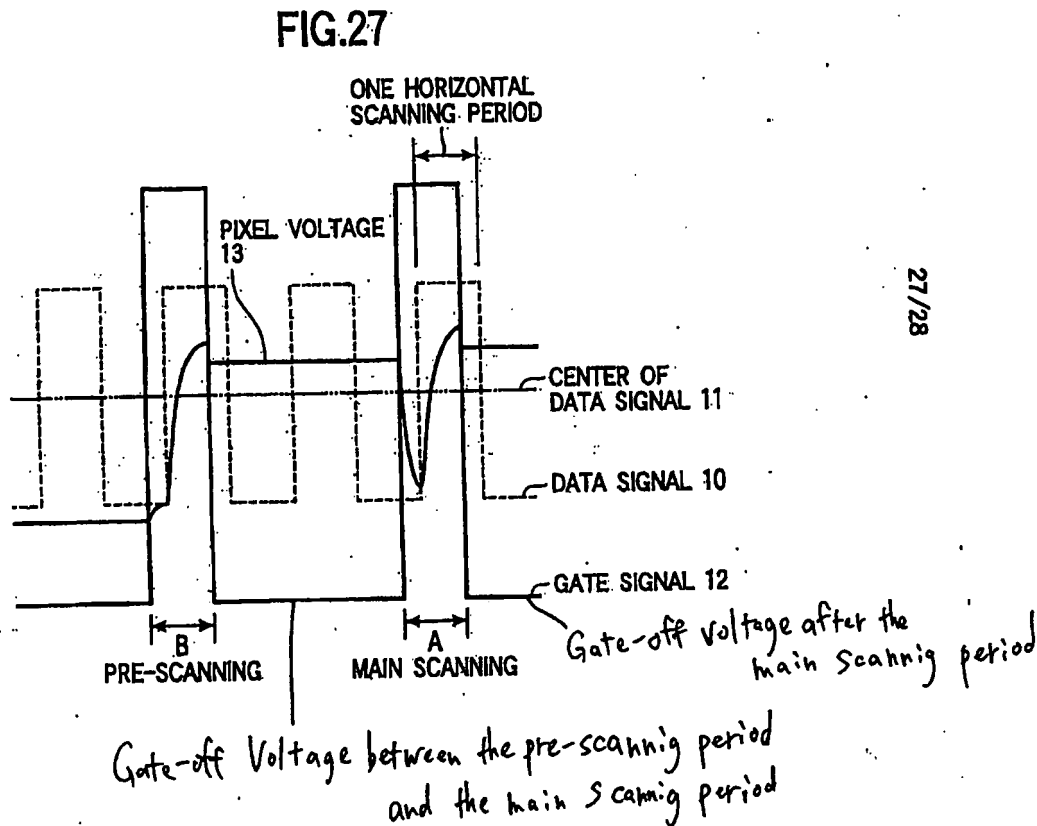
FIG.27



Accordingly, for at least these reasons, withdrawal of this § 102(a) rejection of independent Claim 1 and associated dependent Claims 2 and 12 is respectfully requested.

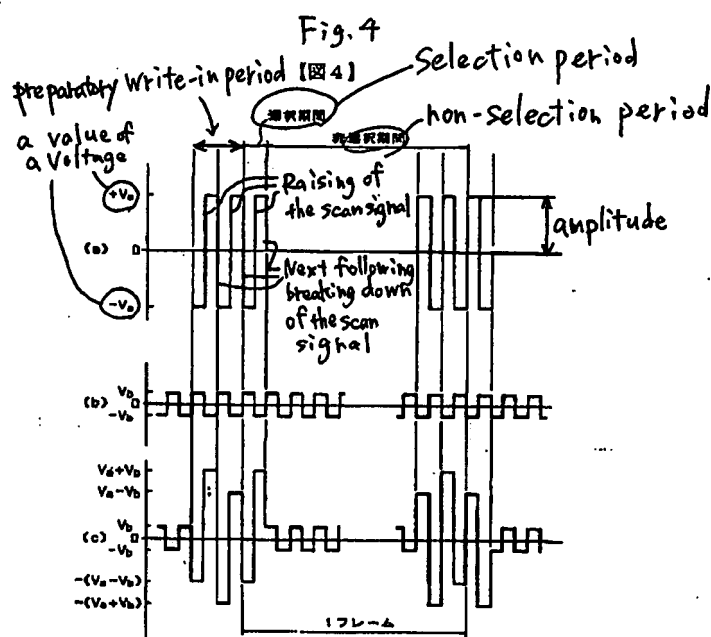
With regard to independent Claim 5, AAPA fails to disclose that the horizontal scanning period has a pre-writing data voltage period where the pre-scanning is performed and the main scanning is not performed, and another period where the main scanning is performed, and a value of a predetermined pre-writing data voltage that is a data voltage in the pre-writing data voltage period is different from a value of a display data voltage that is a data voltage in another period. Accordingly, for at least these reasons, Applicant respectfully requests the withdrawal of this § 102(a) rejection of independent Claim 5 and associated dependent Claims 6-10.

Amended independent Claim 11 recites that a value of a gate-off voltage between the pre-scanning period and the main scanning period is set to be higher than a value of the gate-off voltage after the main scanning period. In contrast, in AAPA, a value of a gate-off voltage between the pre-scanning period and the main scanning period is set to the same as a value of the gate-off voltage after the main scanning period (see, for example, Prior Art Fig. 27 shown below with notations). Therefore, AAPA fails to disclose or suggest this feature of Claim 11. Accordingly, Applicant respectfully requests the withdrawal of this § 102(a) rejection of Claim 11.



Claims 3 and 4 stand rejected under § 103 on the basis of AAPA and Yasukatsu '170. Applicant traverses this rejection for the following reasons.

Independent Claim 3 has been amended, and now recites that a value of an on-voltage of a gate signal in the pre-scanning is different from a value of an on-voltage of the gate signal in the main scanning. In contrast, voltage waveforms illustrated in Fig. 2 (a) and Fig. 4 (c) of Yasukatsu '170 are not that of scan signals (gate signals), but that of signals that are the sum of a scan signal (see Fig. 4(a)) and a data signal, respectively (see Fig. 4(b)). Fig. 4(a) of Yasukatsu '170 (shown below with notations) shows a scan signal. However, values of on-voltage of the scan signal in the preparatory write-in period ($+V_a$ or $-V_a$) are the same as values of on-voltage of the scan signal in the selection period ($+V_a$ or $-V_a$). Accordingly, for at least these reasons, withdrawal of this § 103 rejection of independent Claim 3 is respectfully requested.

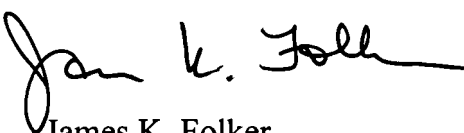


Claim 4, as amended, now recites that a length between timing of raising of a gate signal and timing of next following breaking down of the gate signal in the pre-scanning period is different from that in the main scanning period. In contrast, Yasukatsu '170 discloses that a length between timing of raising of a scan signal and timing of next following breaking down of the scan signal in the preparatory write-in period is different from that in the selection period (see Fig. 4 (a), shown above with notations). Therefore, Yasukatsu '170 fails to disclose or suggest the feature of Claim 4, and this § 103 rejection should be withdrawn.

For the foregoing reasons, Applicant believes that this case is in condition for allowance, which is respectfully requested. The Examiner should call Applicant's attorney if an interview would expedite prosecution.

Respectfully submitted,

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By 

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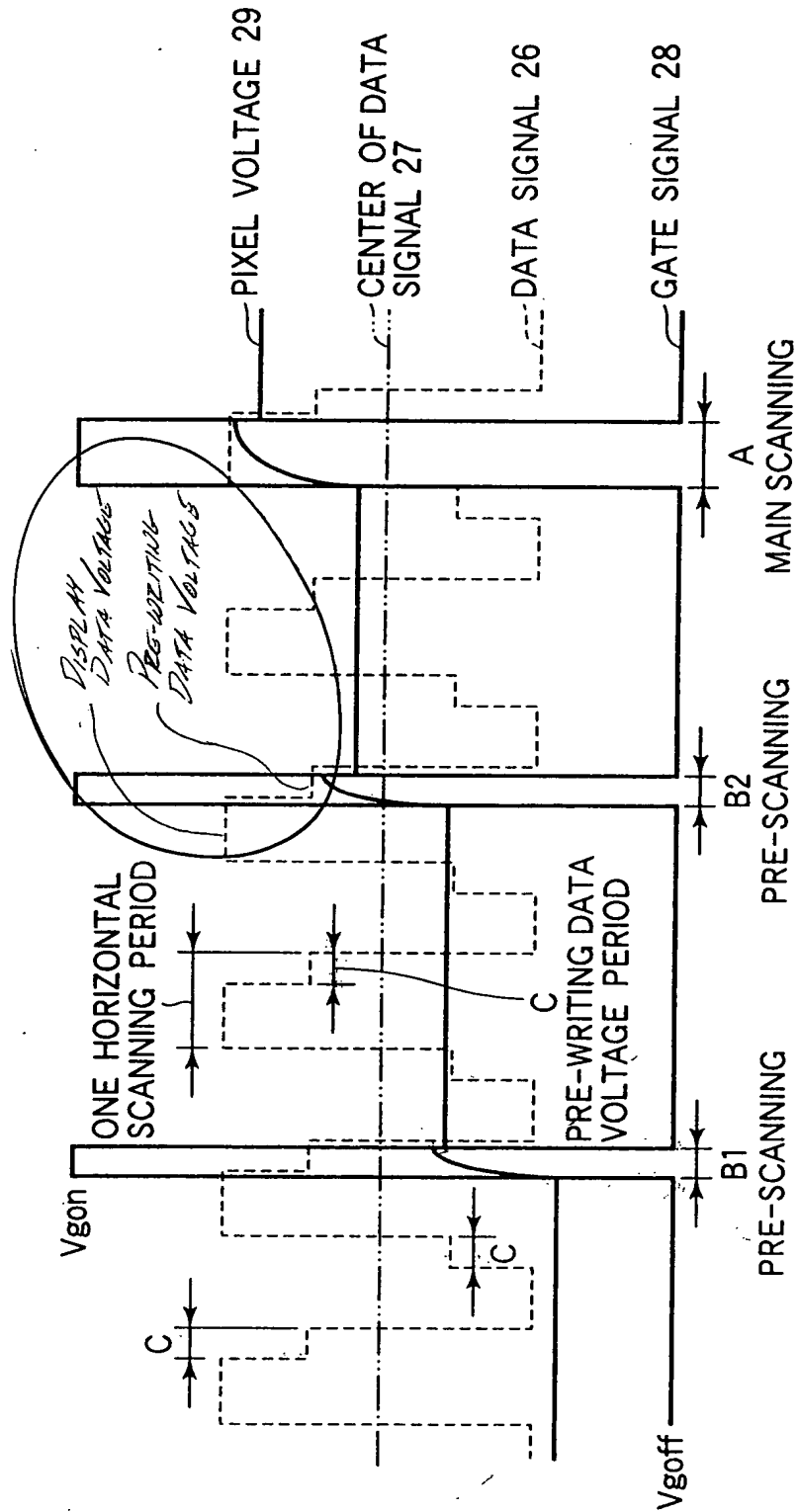
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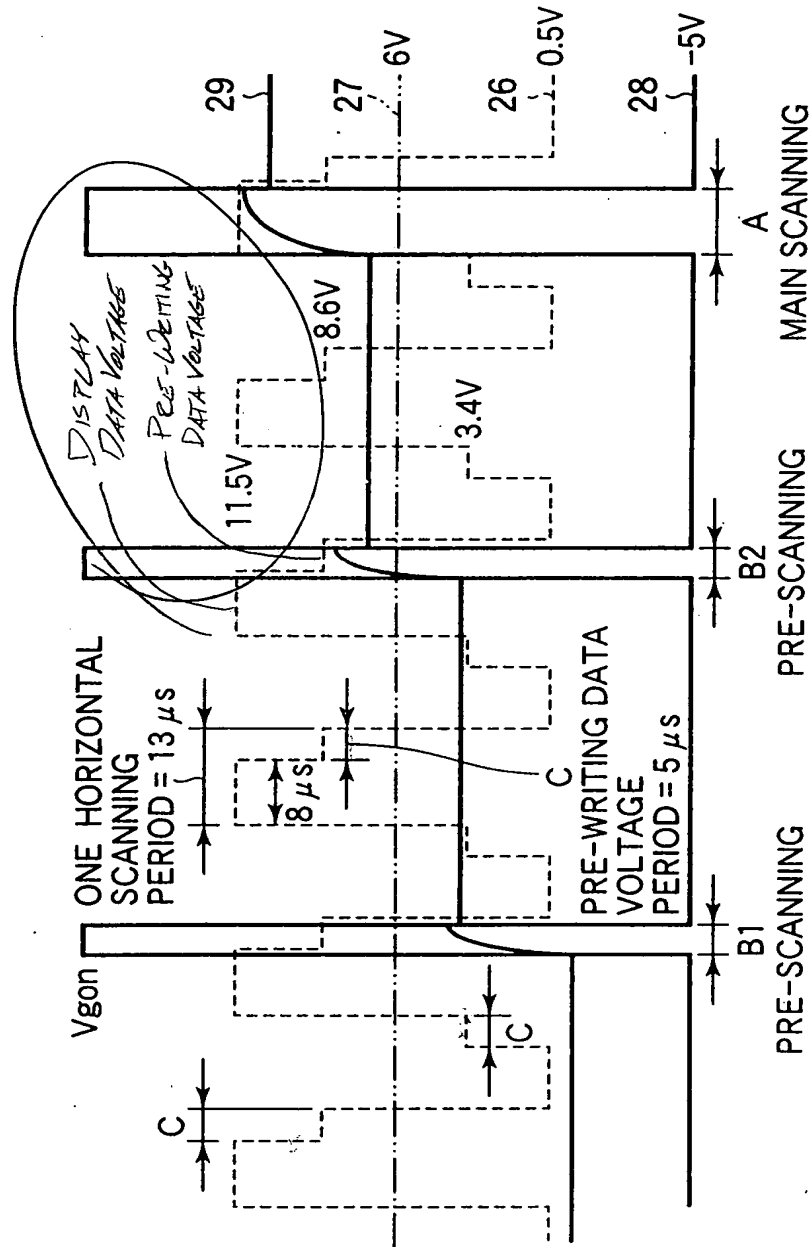
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FIG.9



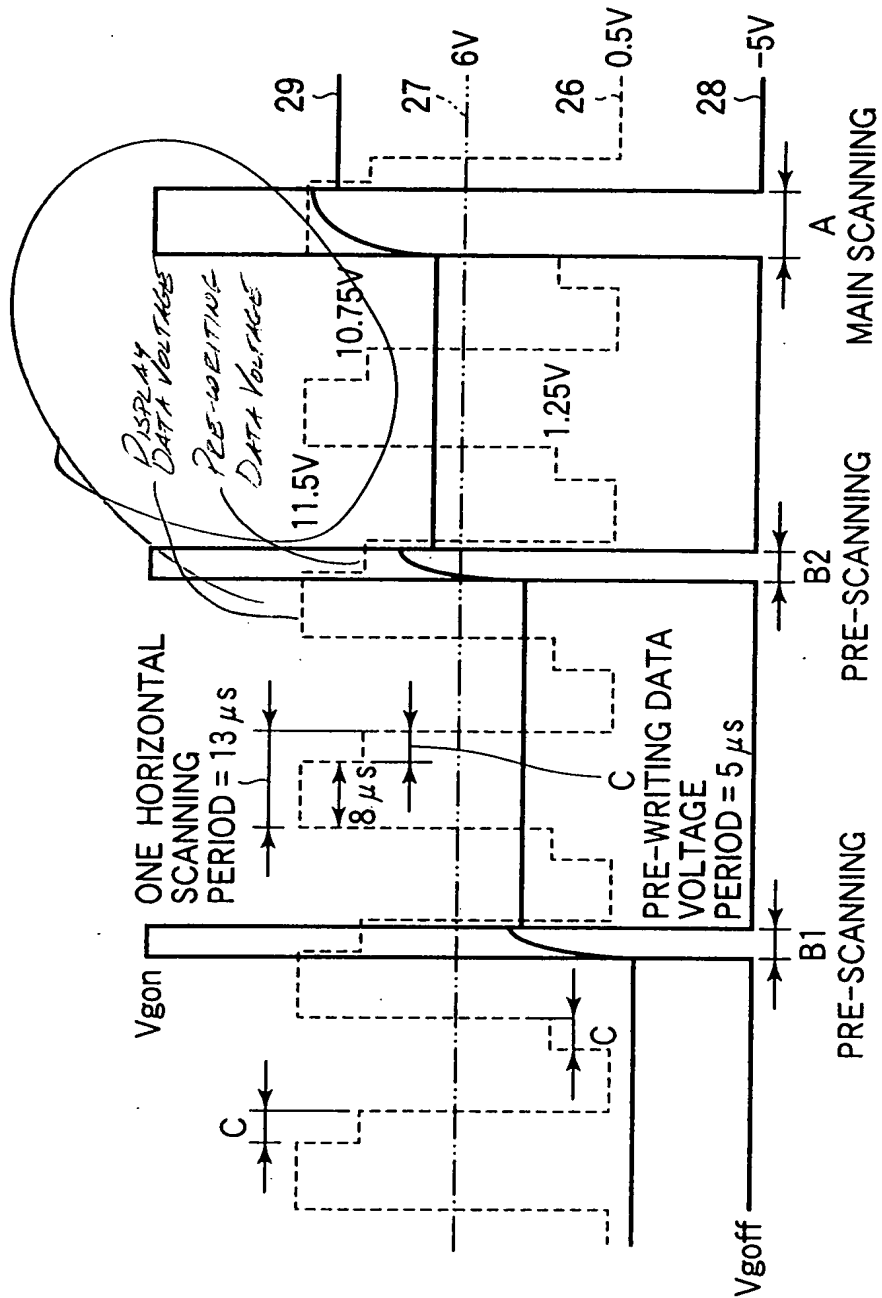
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FIG.11



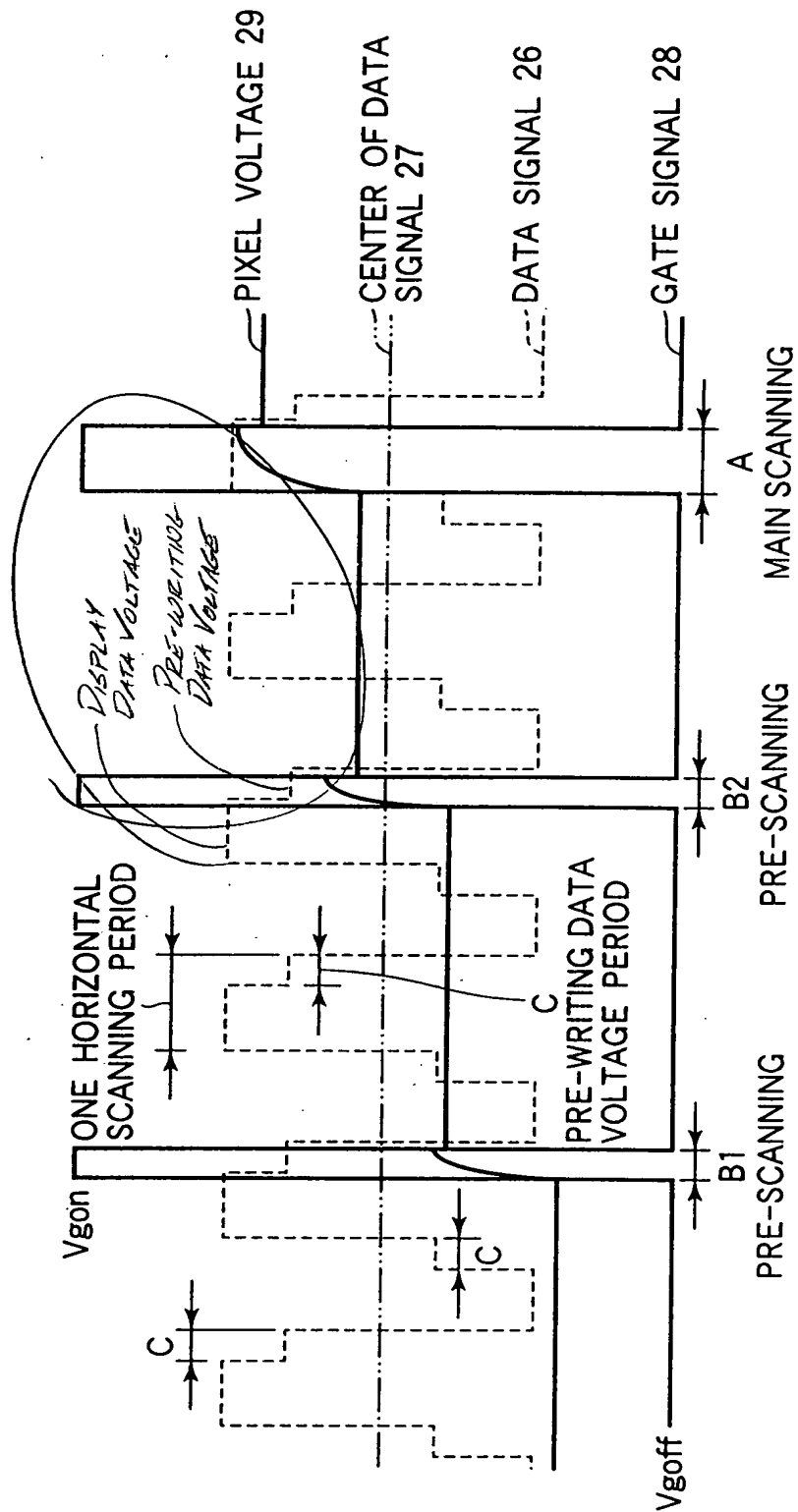
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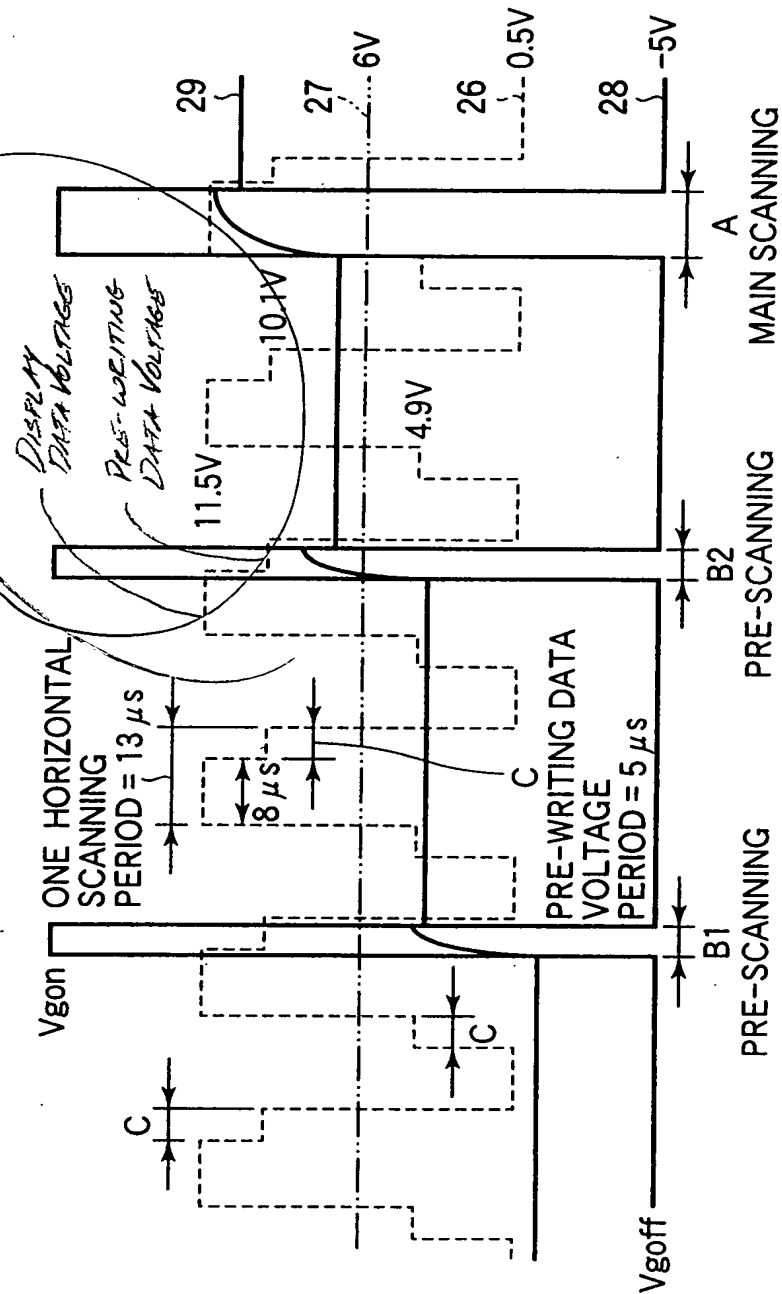
FIG.12



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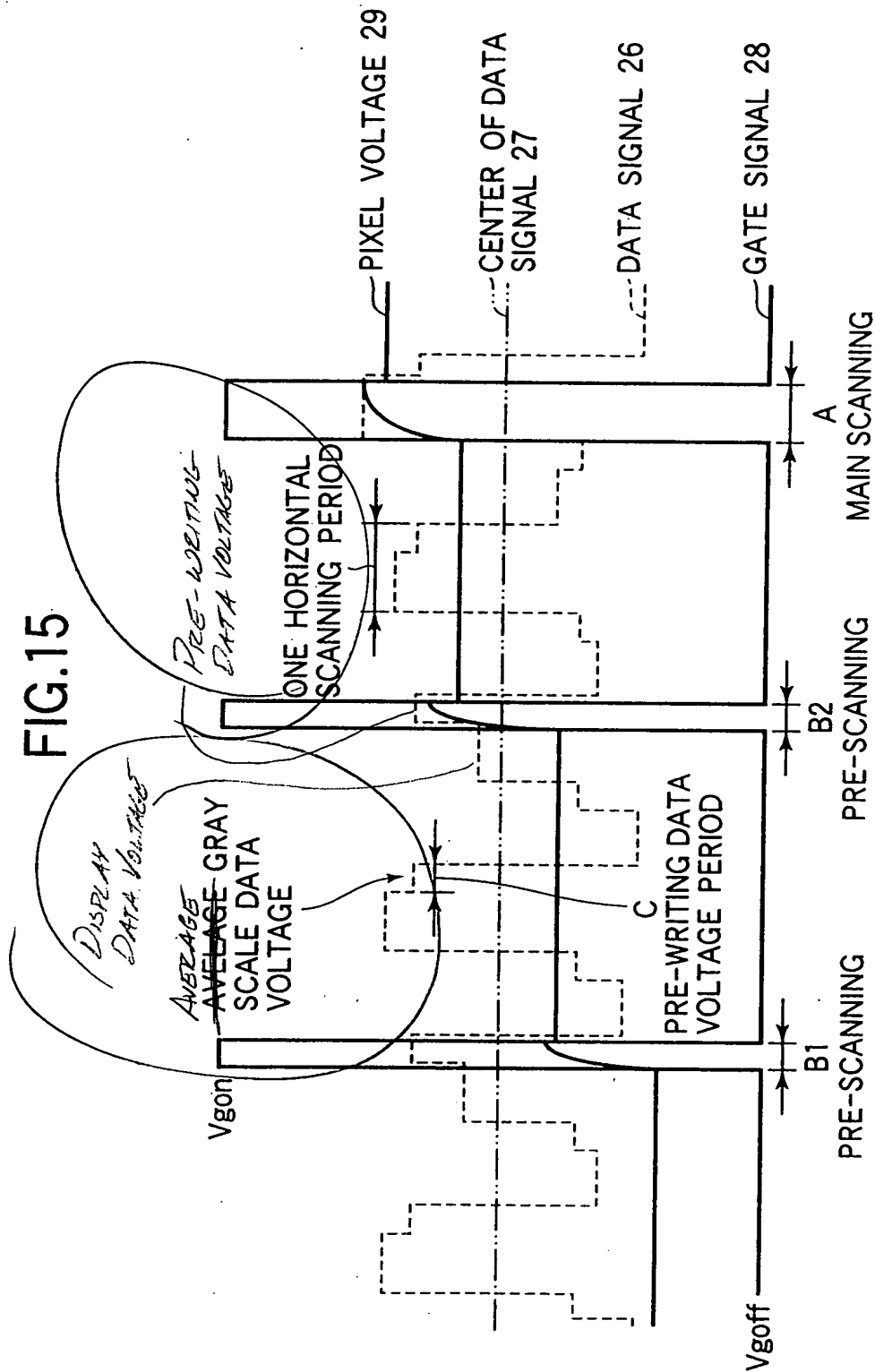
FIG.13





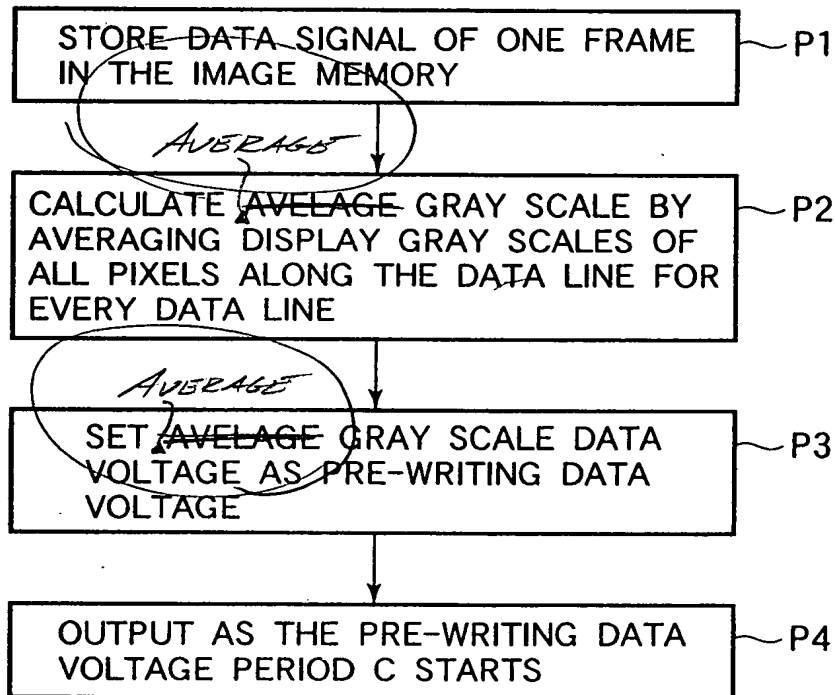
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FIG.15

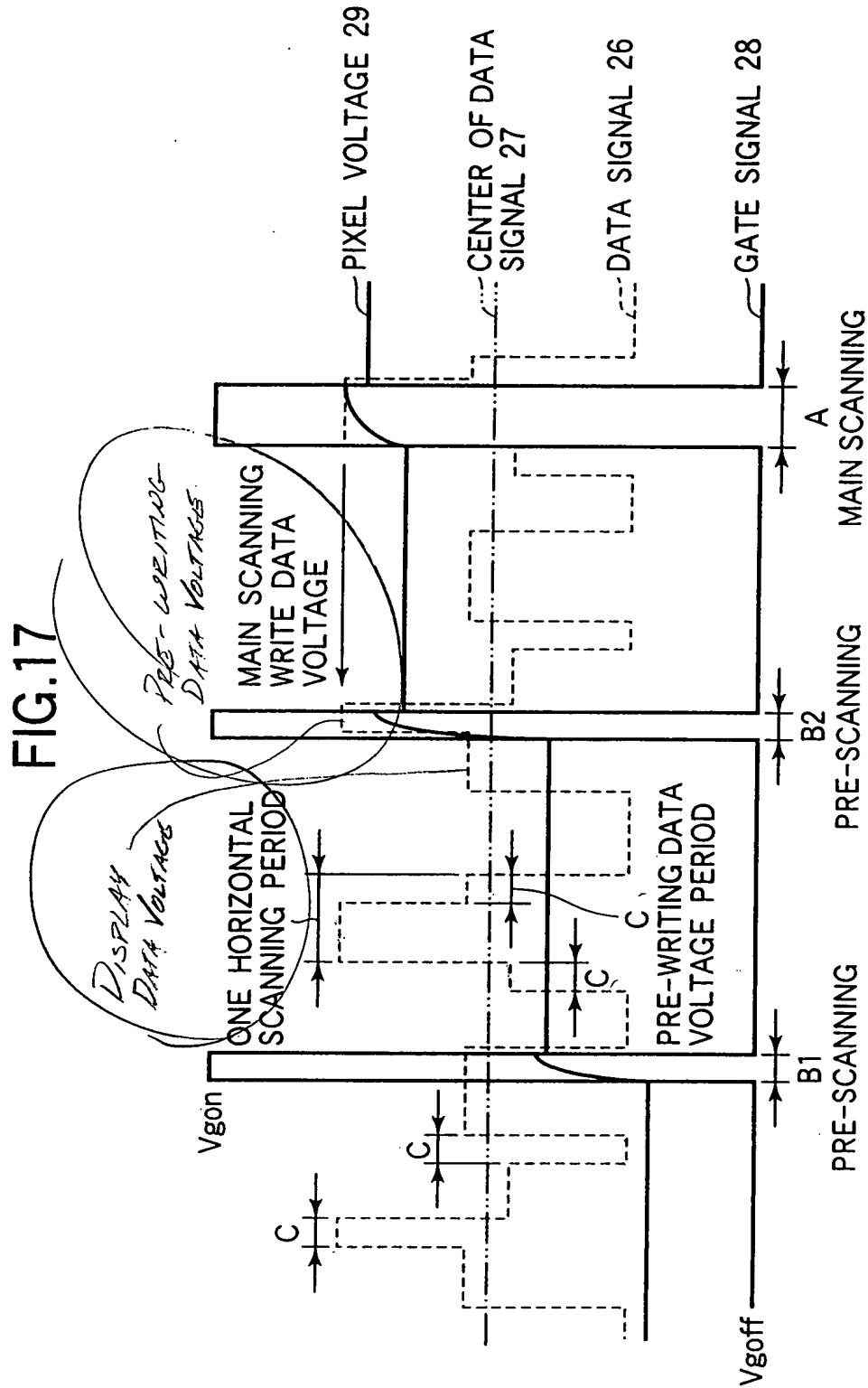


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FIG.16

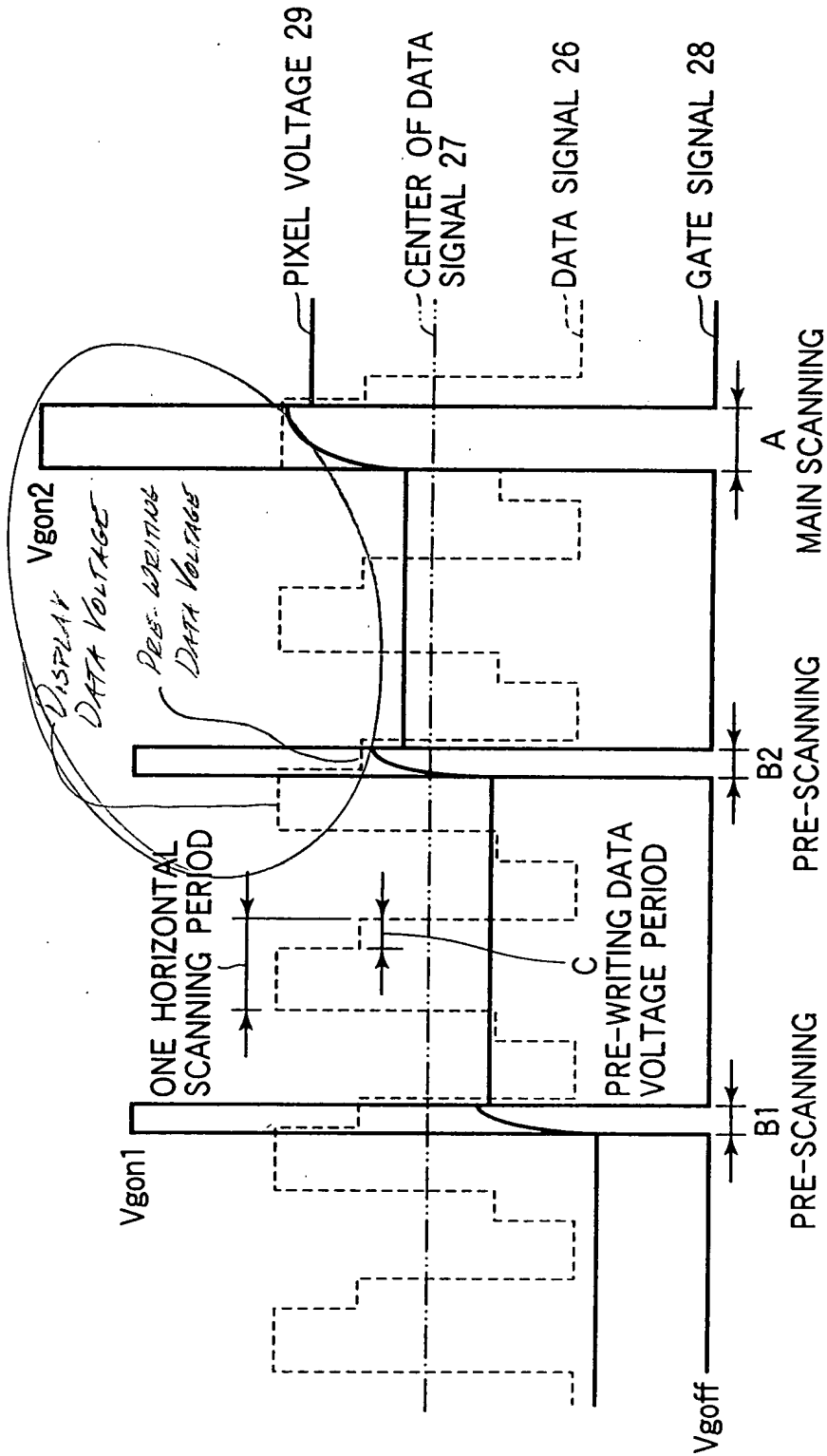


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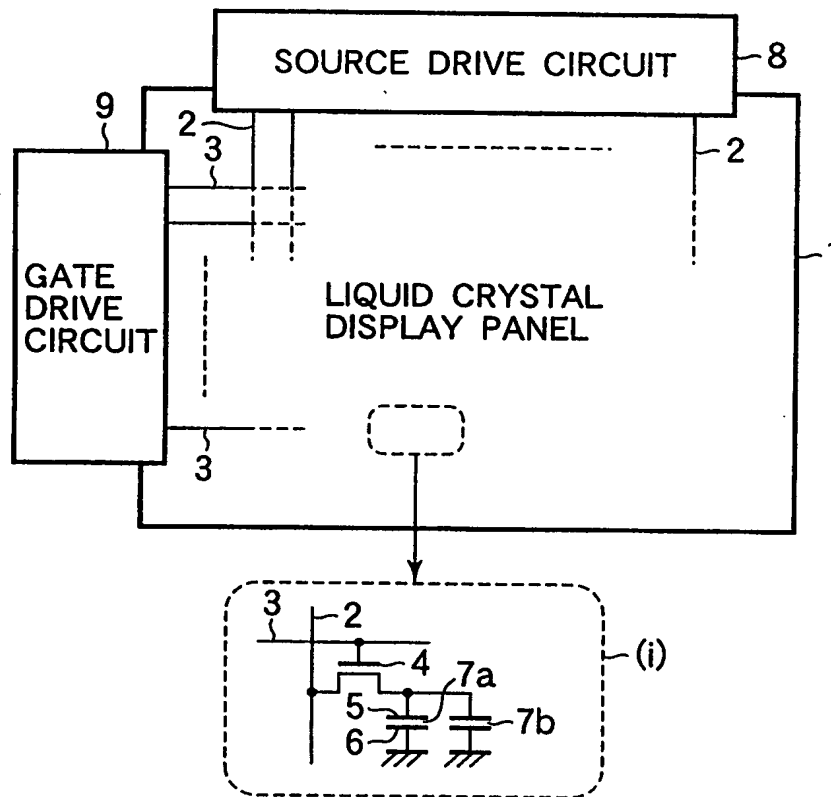
FIG.23



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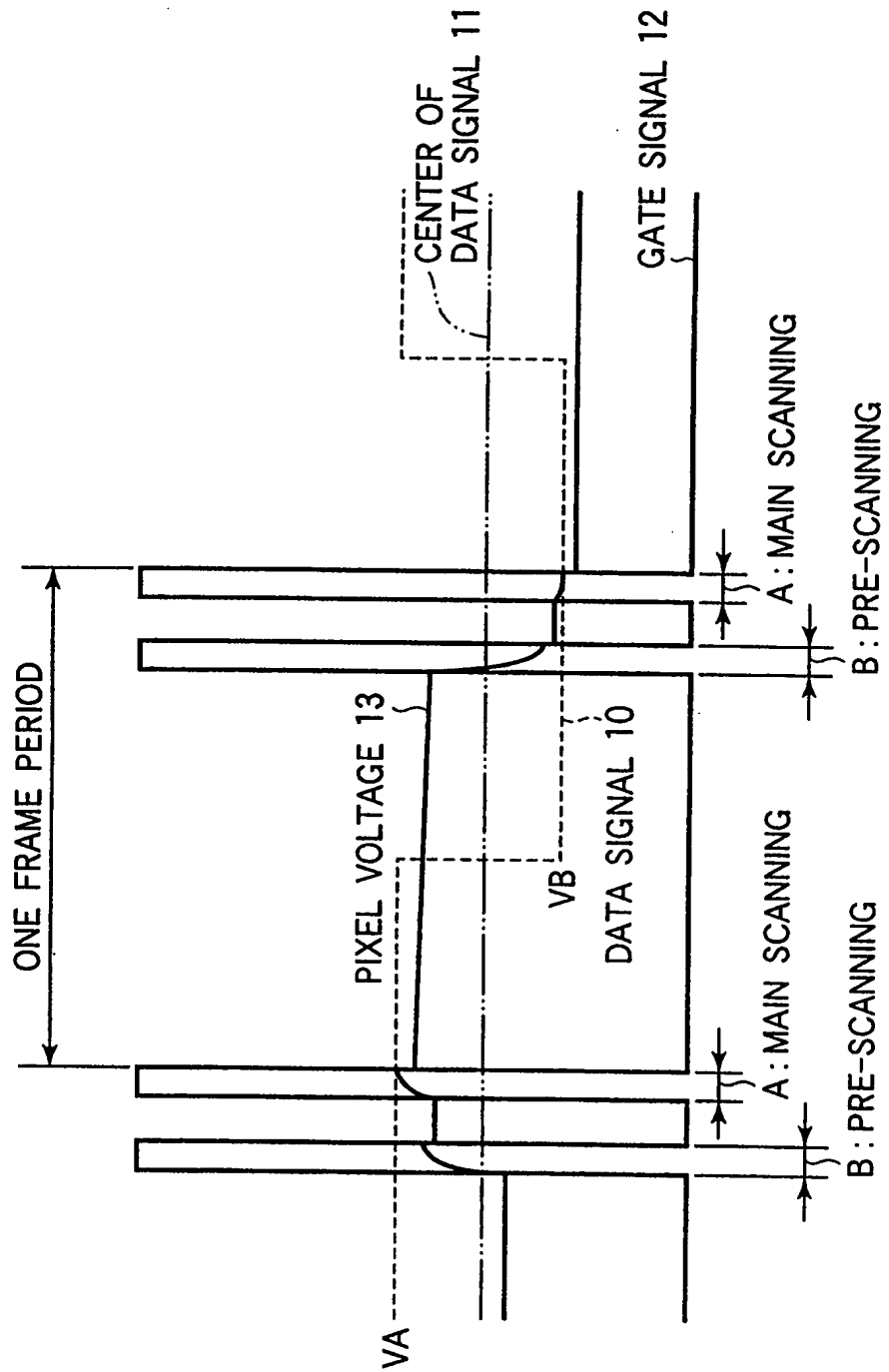


FIG.25



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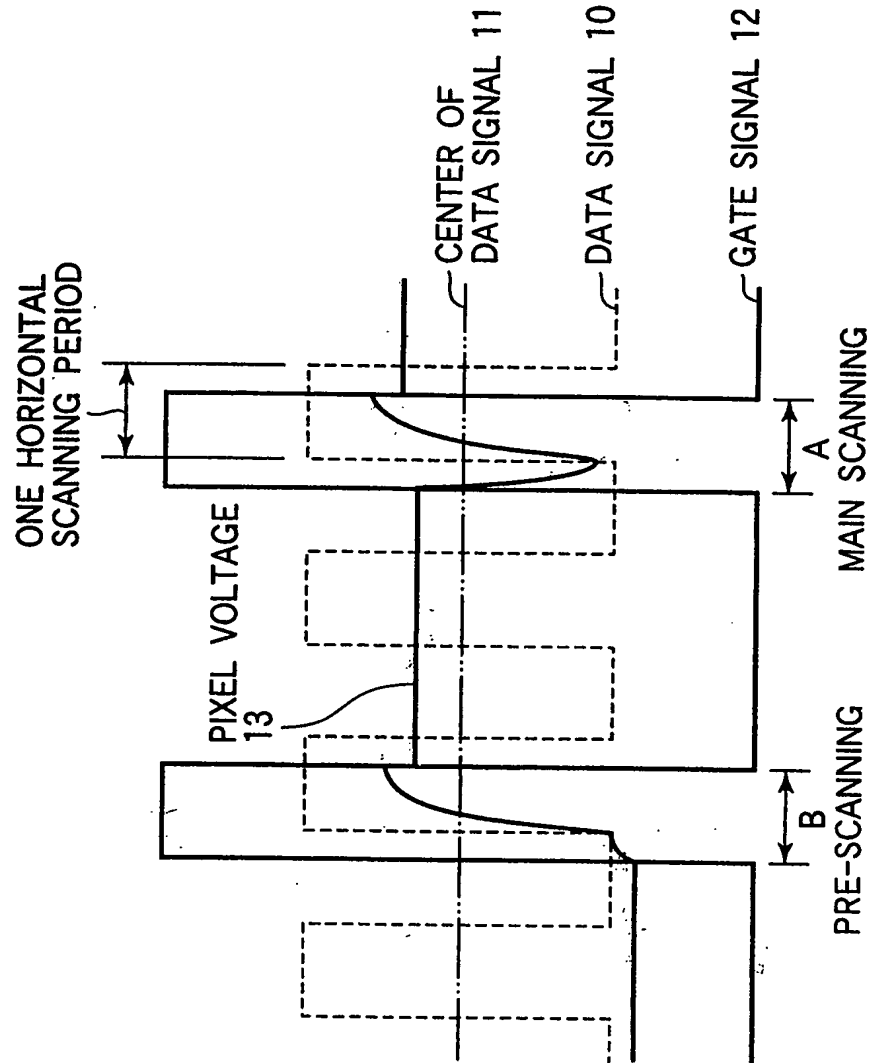
Prove All
FIG.26



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Private

FIG.27



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Provi Art

FIG.28

